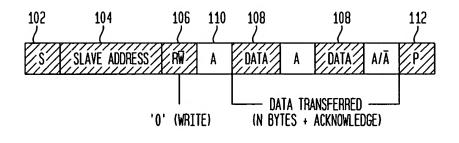


T.E. BAKER 1-11-13

1/3

FIG. 1



FROM MASTER TO SLAVE

A = ACKNOWLEDGE (SDATA LOW)

FROM SLAVE TO MASTER

A = NOT ACKNOWLEDGE (SDATA HIGH)

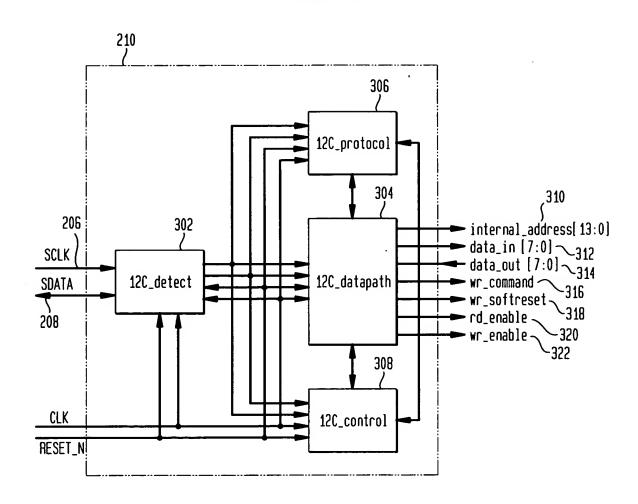
S = START CONDITION

P = STOP CONDITION

FIG. 2 202 204 SLAVE -216 DEVICE PLL 206 210 **SDATA** -212 MASTER DEVICE **INTERFACE MEMORIES** SCLK CIRCUIT 208 -214 INTERNAL REGISTERS

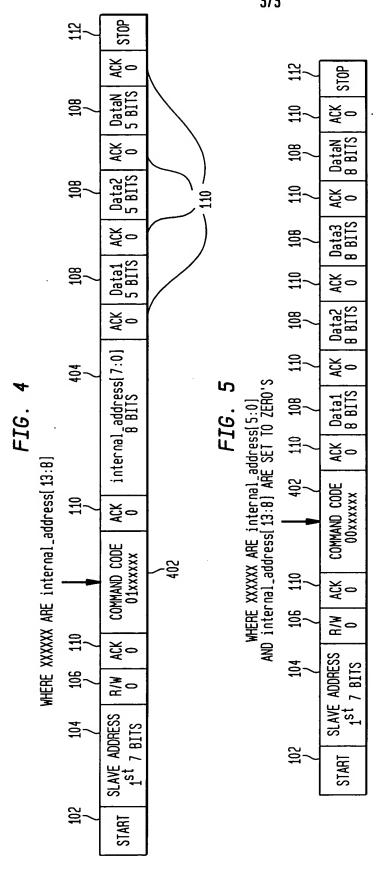
Ì

FIG. 3



į





102 104 106 110 402 110 112 COMMAND TO SELECTED REGISTERS ALAVE ADDRESS R/W ACK COMMAND CODE ACK STOP

FIG. 6